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EEEEEEEEEE	RRRRRRRRRRR	FFFFFFFFFF
EEEEEEEEEE	RRRRRRRRRRR	FFFFFFFFFF
EEEEEEEEEEE	RRRRRRRRRRR	FFFFFFFFFF
EEE	RRR RRR	FFF
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VAX-11 Bliss-32 V4.0-742 LERF.SRCJMCHK_DISP.B32;1

MODULE MCHN_DISP (%TITLE 'Machine check dispatcher' IDENT = 'V04-000') =

BEGIN

0027 0028

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FACILITY: ERLRPT, Error Log Report Generator

ABSTRACT:

This module contains the machine check dispatching routine.

ENVIRONMENT:

VAX/VMS operating system, user mode.

AUTHOR: Sharon A. Reynolds, CREATION DATE: 10-Apr-1984

Modified by:

V04-003 SAR0290 Sharon A. Reynolds 17-Jul-1984 Changed the way that option is referenced due to change in the way it is passed.

V04-002 SAR0256 25-apr-1984 Sharon A. Reynolds Put in check to determine whether to output text.

EAD0139 Elliott A. Drayton Removed reference to LIB.REQ. V04-001 EAD0139 12-Apr-1984

```
MCHK_DISP
V04-000
                                                                                                                                  VAX-11 B'iss-32 V4.0-742 

[ERF.SRC]MCHK_DISP.B32;1
                       Machine check dispatcher
                                                                                                                                                                                        Page
                       0058
0344
0345
0346
0347
                                   REQUIRE 'SRC$: ERFDEF.REQ';
     MACRO
                                         Cpu_fld = 0,24,8,0 %;
                                   FORWARD ROUTINE Mchk_dispatcher:
                                                                       Novalue ;
                        0350
                                   EXTERNAL ROUTINE
                       0352
0353
0354
0355
                                          Frctof,
                                          Header,
                                         Logger,
Mcheck_7ZZ,
Mcheck_750,
Mcheck_780,
Mcheck_UV1;
                       0356
                       0357
                       0358
                       0359
                                   EXTERNAL Emb:
                       0360
                       0361
                                                           $BBLOCK PSECT (emb);
                       0362
```

```
MCHK DISP
                                                                         15-Sep-1984 23:51:45
14-Sep-1984 12:27:43
                  Machine check dispatcher
                                                                                                     VAX-11 Bliss-32 V4.0-742
                                                                                                                                              Page
                                                                                                    [ERF.SRC]MCHK_DISP.B32;1
V04-000
                  0363
                           GLOBAL Routine MCHK_DISPATCHER (lun,option) : NOVALUE =
                  0364
0365
0366
0367
    08
                           Begin
    81
    82
                  0368
                             Functional Description:
    85
                  0369
                  0370
    86
                                 This routine outputs the machine check header, then via the
    87
                  0371
                                 cpu-type determines which cpu dependent routine to invoke and
                  0372
0373
    88
                                 invokes the routine.
    89
    90
                             Calling Sequence:
    91
    92
93
                  0376
                                 MCHK_DISPATCHER (lun,option)
                  0377
    94
                  0378
                              Input Parameters:
    95
                  0379
    96
                  0380
                                 Lun = logical unit number of output device.
    97
                  0381
                                 Option = address of descriptor that contains the option.
    98
                  0382
    99
                  0383
                             Output Parameters:
   100
                  0384
                  0385
   101
                                 None.
                  0386
   102
   103
                  0387
                  0388
   104
   105
                  0389
                           LOCAL
   106
                  0390
                                Cpu_type:
                                             REF $BBLOCK :
                  0391
   107
                  0392
0393
   108
   109
                                Option:
                                             REF $BBLOCK :
   110
                  0394
                  0395
   111
                           Bind start_of_cpu_specific_stuff = emb[emb$b_mc_sumcod];
                  0396
   112
   113
                  0397
                  0398
   114
                             Determine whether to output any text.
                  0399
   115
                           if ..option[dsc$a_pointer] NEQ 'S'
                  0400
   116
   117
                  0401
   118
                  0402
                                Return ;
                  0403
   119
   120
                  0404
   121
122
123
124
125
                  0405
                             Force a top of form to occur, and output the entry header.
                  0406
                           FRCTOF (.lun);
HEADER (.lun);
LOGGER (.lun, %ASCID 'MACHINE CHECK');
                  0407
                  0408
                  0409
   126
127
                  0410
                  0411
   128
129
130
                  0412
                             Get the cpu type field from the system id register.
                  0414
                           Cpu_type = emb[emb$l_hd_sid] ;
   131
132
133
                  0415
                           Cpu_type = .cpu_type[cpu_fld];
                  0416
                  0417
   134
                             Determine if the cpu type is an 11/780 without the cpu field
                  0418
   135
                             in the system id set.
```

```
MEM
```

```
MCHK_DISP
V04-000
                                                                                                     VAX-11 Bliss-32 V4.0-742
LERF.SRCJMCHK_DISP.B32;1
                  Machine check dispatcher
   136
137
                           if .cpu_type EQLU 255
   138
                           Then
   139
                                  Yes, call the 11/780 machine check module to output
   140
                                  the rest of the report.
   141
   142
                                MCHECK_780 (.lun,start_of_cpu_specific_stuff);
   144
                              Determine if the cpu type is any other valid type. If a valid/supported
   146
147
148
149
                              cpu type is found then call the cpu dependent machine check
                  0433
0433
0433
0434
0435
                              moduie.
                           Case .cpu_type from 1 to 7 of
   150
151
152
153
154
                                Set
[1]: MCHECK_780 (.lun,start_of_cpu_specific_stuff);
                                [2]: MCHECK_750 (.lun,start_of_cpu_specific_stuff) ;
                  0438
   155
                  0439
                                [3]: MCHECK_7ZZ (.lun,start_of_cpu_specific_stuff);
   156
157
                  0440
                  0441
                                [7]: MCHECK_UV1 (.lun,start_of_cpu_specific_stuff);
                  0442
0443
   158
   159
                                [INRANGE]: Return:
                  0444
   160
                                Tes ;
                  0445
   161
                  0446
   162
                           End:
                                    ! Routine
                                                                                     .TITLE
                                                                                              MCHK_DISP Machine check dispatcher
                                                                                     .IDENT
                                                                                              \V04=000\
                                                                                     .PSECT
                                                                                             $PLIT, NOWRT, NOEXE, PIC, 2
                                                                    00000 P.AAB:
                          43
                               20 45
                                        4E 49
                                                                4D
                                                                                             \MACHINE CHECK\<0><0><0>
                                                  48
                                                                                     .ASCII
                                                                00
                                                                     0000F
                                                         010E000D
                                                                     00010 P.AAA:
                                                                                             17694733
                                                                                     .LONG
                                                         ,00000000
                                                                    00014
                                                                                     .ADDRESS P.AAB
                                                                                             FRCTOF, HEADER, LOGGER MCHECK_722, MCHECK_750 MCHECK_UV1
                                                                                     .EXTRN
                                                                                     .EXTRN
                                                                                     .EXTRN
                                                                                     .EXTRN
                                                                                     .PSECT
                                                                                              $CODE, NOWRT, PIC, 2
                                                               003C 00000
                                                                                     .ENTRY
                                                                                              MCHK_DISPATCHER, Save R2,R3,R4,R5
                                                                                                                                                  0363
                                                0000000G
                                                                 9E
                                                                    00002
                                                                                              MCHECK_780, R5
                                                                                     MOVAB
                                                                                             START_OF_CPU_SPECIFIC_STUFF, R4
                                             54
                                                0000000G
                                                            00
                                                                    00009
                                                                                     MOVAB
                                             50
                                                            AC
                                                                                              OPTION, RO
                                                                                                                                                  0400
                                                                 D0
                                                                    00010
                                                                                     MOVL
                                00000053
                                            8F
                                                       Ŏ4
                                                            BO
                                                                 D1
                                                                    00014
                                                                                     CMPL
                                                                                              a4(R0), #83
                                                                 12
                                                             70
                                                                    0001C
                                                                                     BNEQ
                                                                                              7S
                                                                                             LUN, R3
                                                                                                                                                  0407
                                            53
                                                       04
                                                                    0001E
                                                                                     MOVL
                                                                    00022
                                                                 DD
                                                                                    PUSHL
                                                            01
53
                                0000000G
                                                                 FB
                                                                                              W1. FRCTOF
                                            00
                                                                                     CALLS
                                                                    0002B
                                                                                                                                                  0408
                                                                 DD
                                                                                     PUSHL
                                0000000G
                                                                 FB
                                                                    0002D
                                                                                     CALLS
                                                                                              #1, HEADER
```

MCHK_DISP VO4-000	F 2 Machine check dispatcher 15-Sep-1984 23:51:45 VAX-11 Bliss-32 V4.0-742 14-Sep-1984 12:27:43 [ERF.SRCJMCHK_DISP.B32;1	Page 5 (2)
0031	00000000 00 9F 00034 PUSHAB P_AAA 000000000 00 02 FB 0003C CALLS #2, LOGGER 52 FO A4 9E 00043 MOVAB EMB, CPU_TYPE 52 03 A2 9A 00047 MOVZBL 3(CPU_TYPE), CPU_TYPE 000000FF BF 52 D1 0004B CMPL CPU_TYPE, #255 05 12 00052 BNEQ 1\$ 18 BB 00054 PUSHR #^M <r3,r4> 06 01 52 CF 00059 1\$: CASEL CPU_TYPE, #1, #6 001E 0014 000E 0005D 2\$: .WORD 3\$-7\$,- 0028 0031 0031 00065</r3,r4>	0409 0414 0415 0421 0426 0433
	7\$-2\$ 6\$-2\$ 7\$-2\$ 6\$-2\$ 7\$-2\$ 6\$-2\$ 7\$-2\$ 6\$-2\$ 7\$-2\$ 6\$-2\$ 7\$-2\$ 6\$-2\$.	0435 0437 0439
; Routine Size:	18 BB 00085 6\$: PUSHR #^M <r3,r4> 00000000G 00 02 FB 00087 CALLS #2, MCHECK_UV1 04 0008E 7\$: RET</r3,r4>	0441
: 163 : 164 : 165	0447 1 0448 1 End ! Module 0449 0 ELUDOM	1
Name SPLIT SCODE	PSECT SUMMARY Bytes Attributes 24 NOVEC,NOWRT, RD ,NOEXE,NOSHR, LCL, REL, CON, PIC,ALIGN(2) 143 NOVEC,NOWRT, RD , EXE,NOSHR, LCL, REL, CON, PIC,ALIGN(2)	
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MCHK_DISP VO4-000 VAX-11 Bliss-32 V4.0-742 LERF.SRCJMCHK_DISP.B32;1 Machine check dispatcher Page 6 (2) ; _\$255\$DUA28:[SYSLIB]LIB.L32;1 18619 23 1000 00:01.9 COMMAND QUALIFIERS BLISS/CHECK=(FIELD,INITIAL,OPTIMIZE)/LIS=LIS\$:MCHK_DISP/OBJ=OBJ\$:MCHK_DISP MSRC\$:MCHK_DISP/UPDATE=(ENH\$:MCHK_DISP) 143 code + 24 data bytes 00:07.1 00:14.5 Size: Run Time: Elapsed Time: Lines/CPU Min: Lexemes/CPU-Min: 21801 : Memory Used: 84 pages : Compilation Complete

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